

FIG. 1

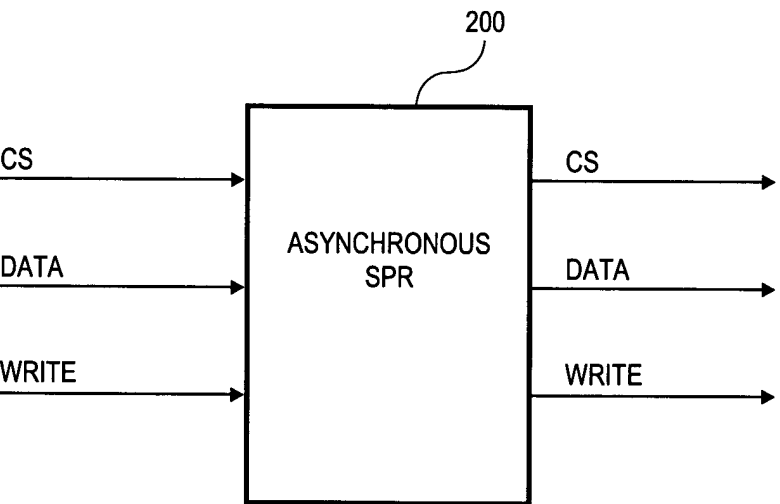


FIG. 2

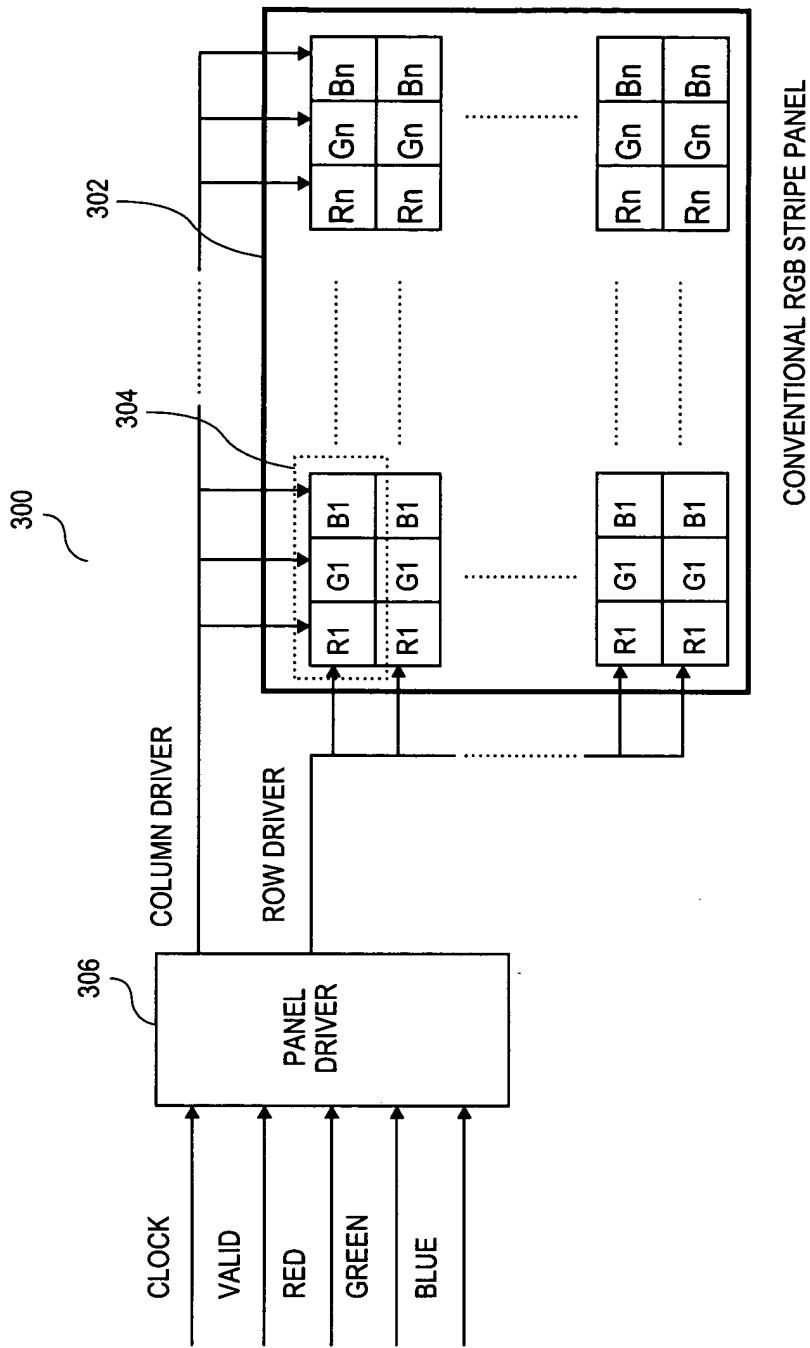


FIG. 3

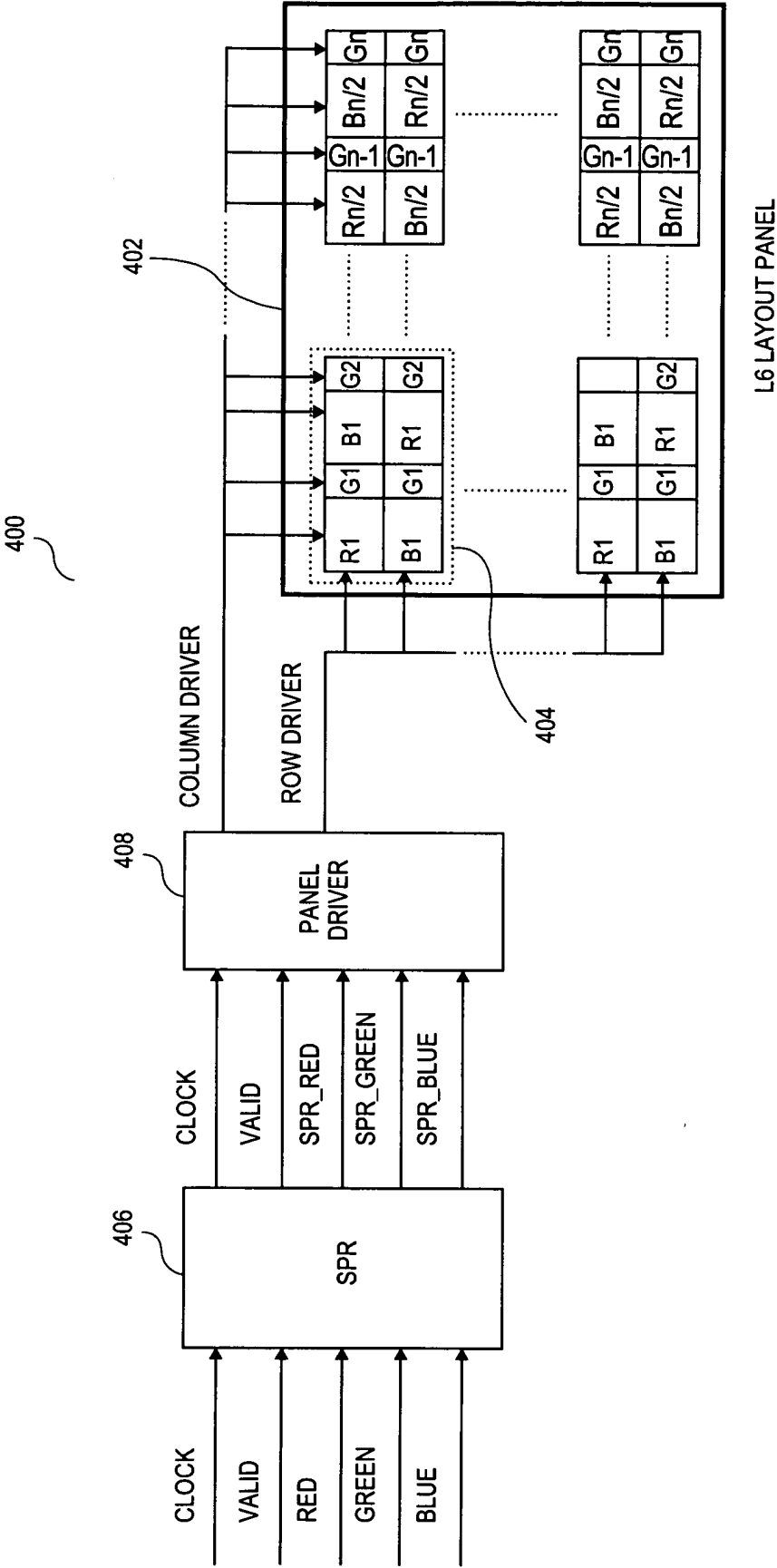


FIG. 4

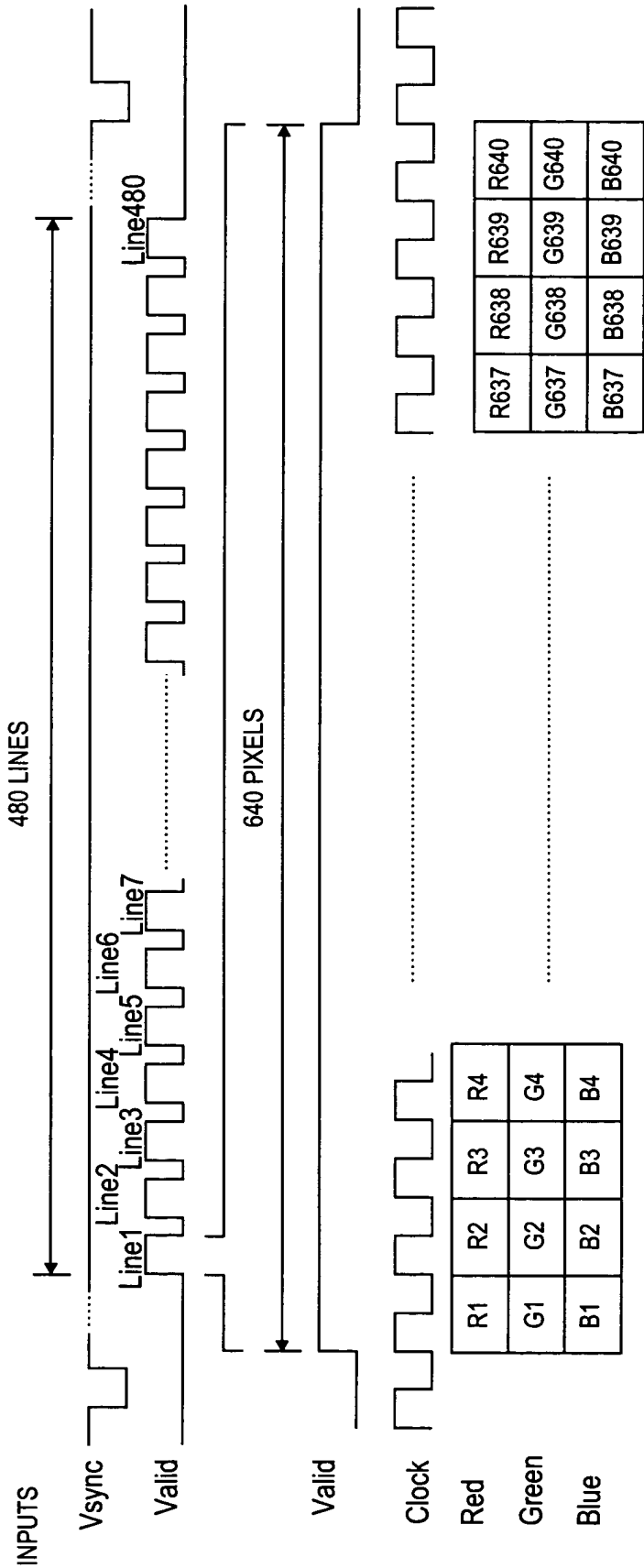


FIG. 5

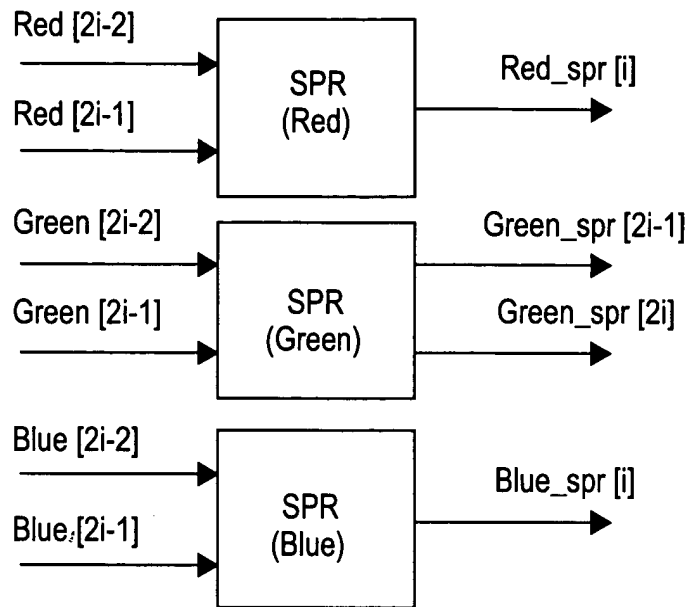


FIG. 6

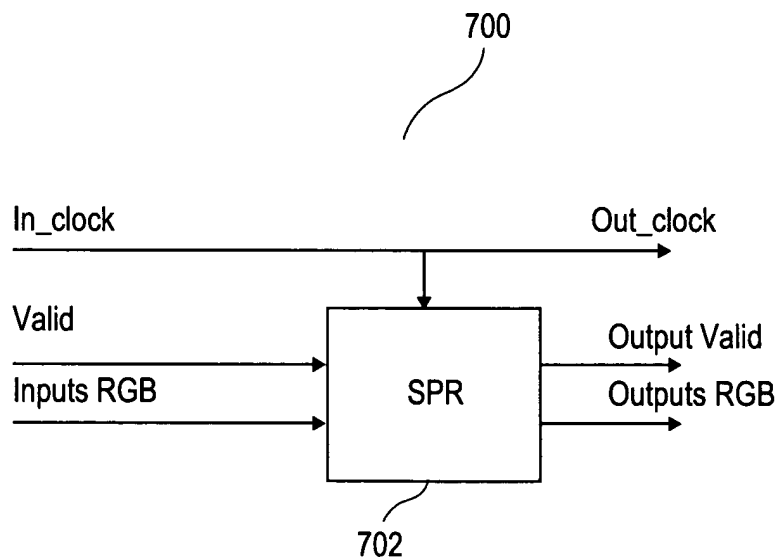


FIG. 7

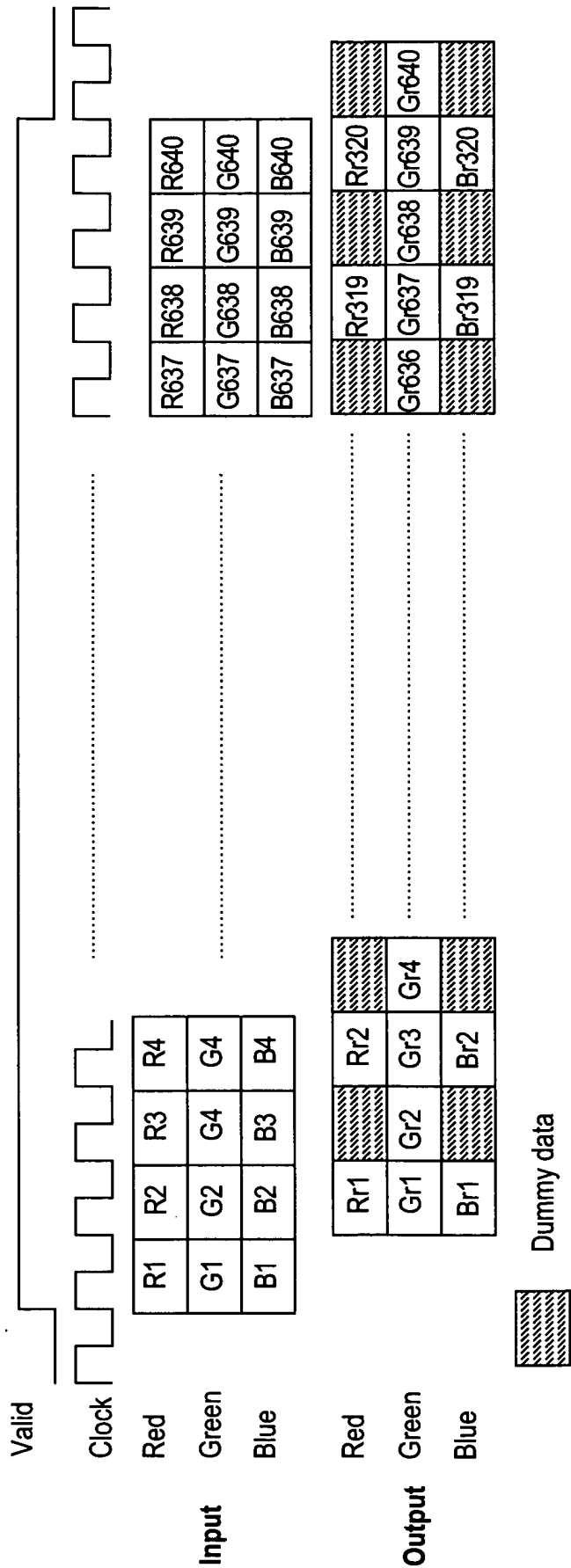


FIG. 8

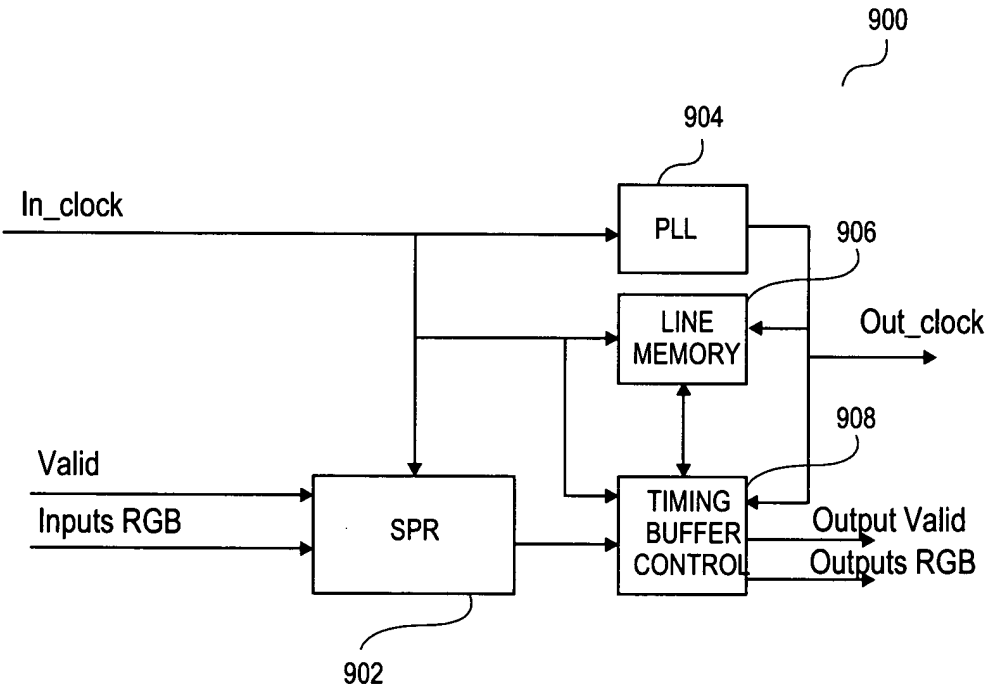
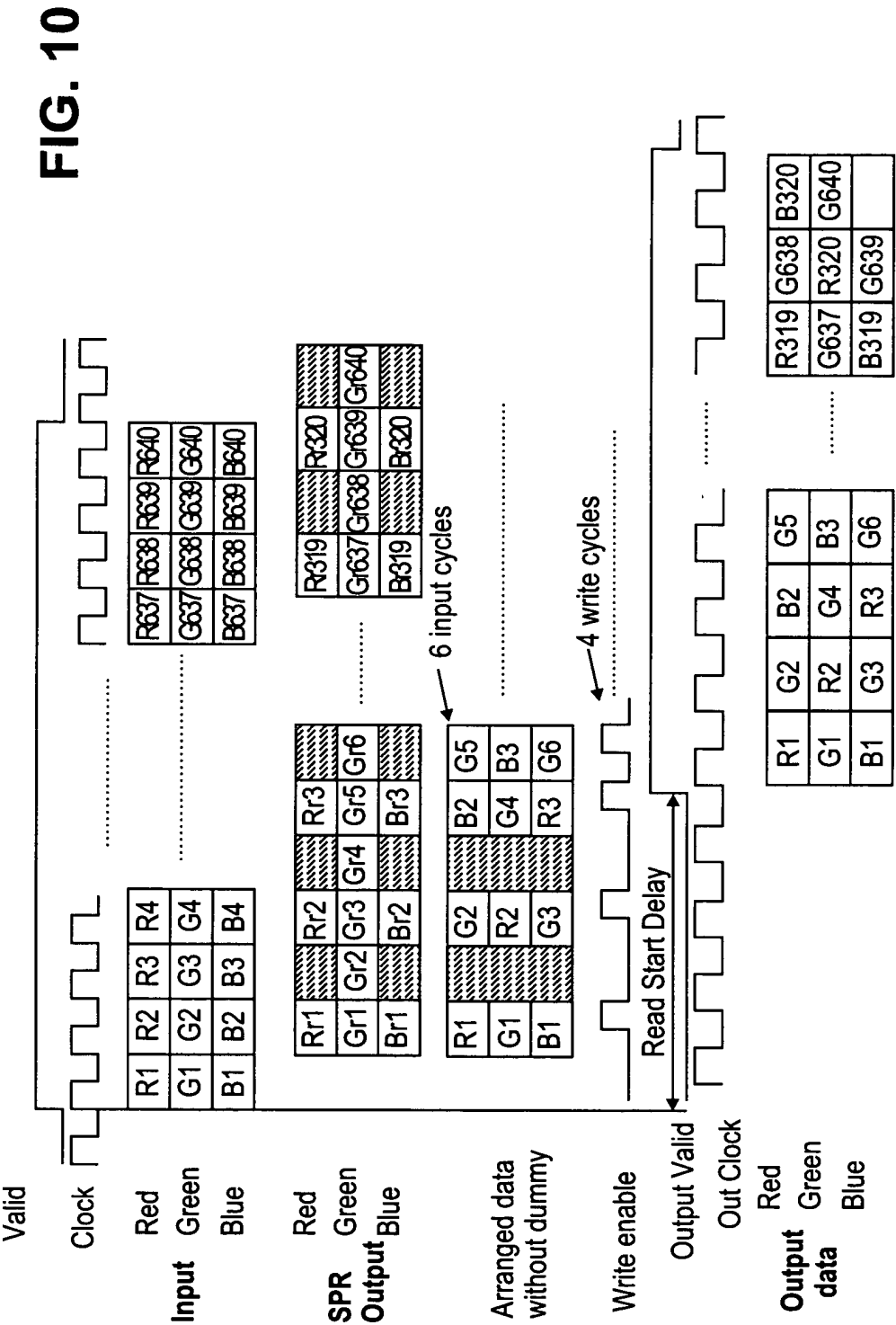


FIG. 9



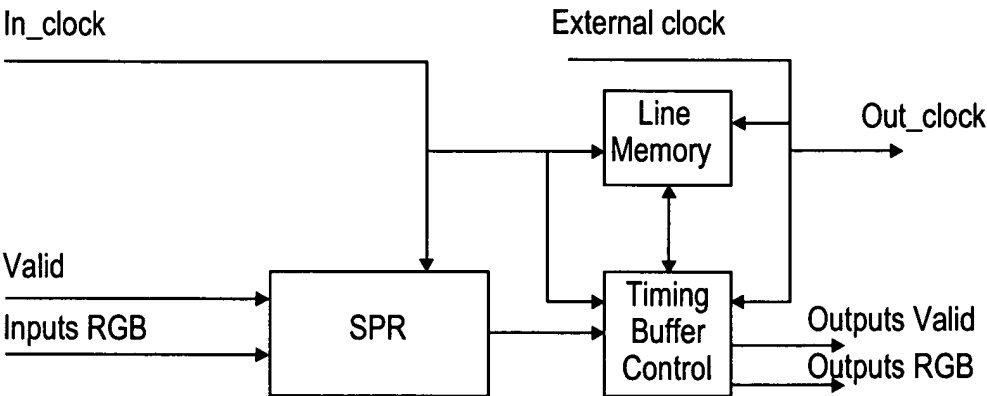


FIG. 11

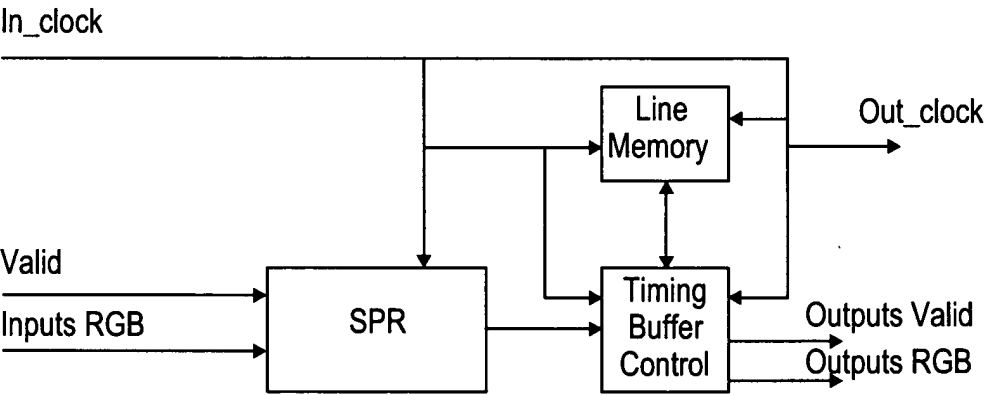


FIG. 12

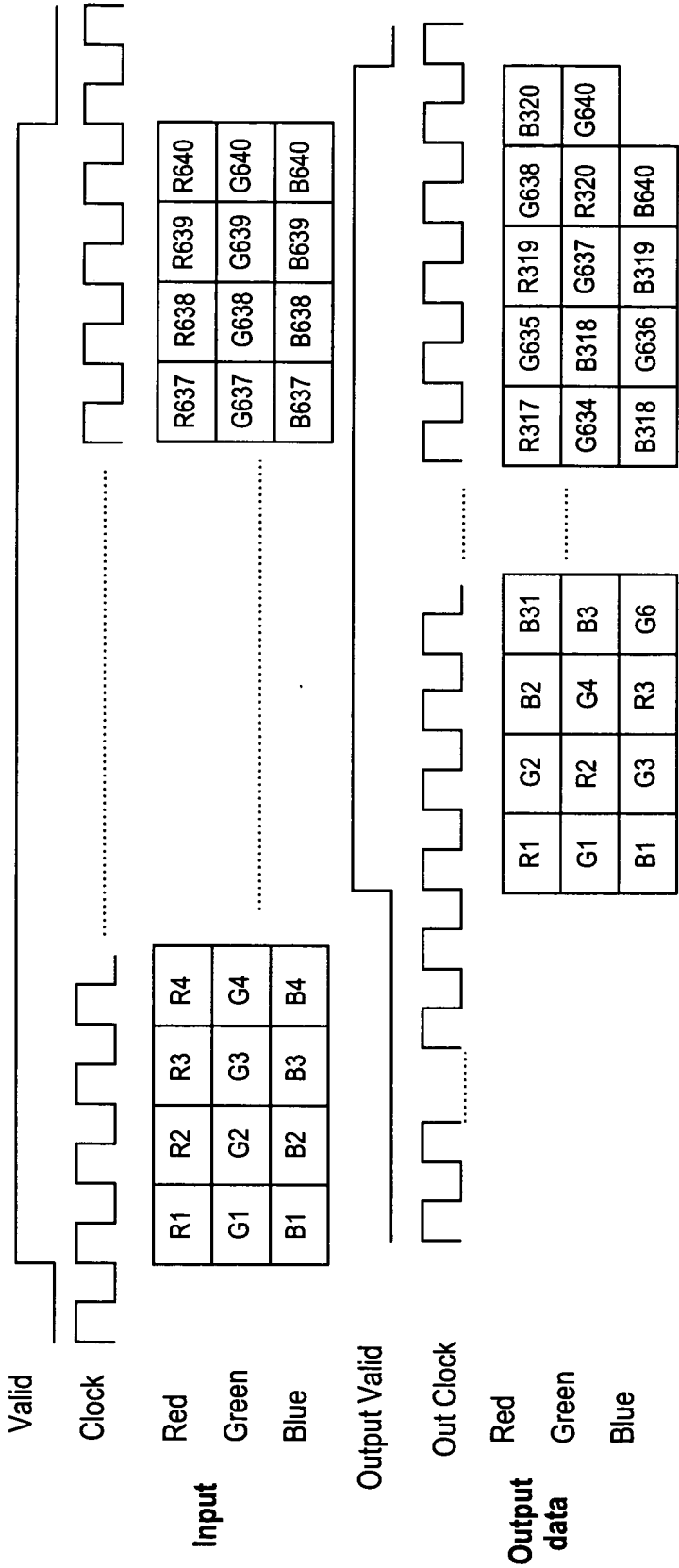


FIG. 13

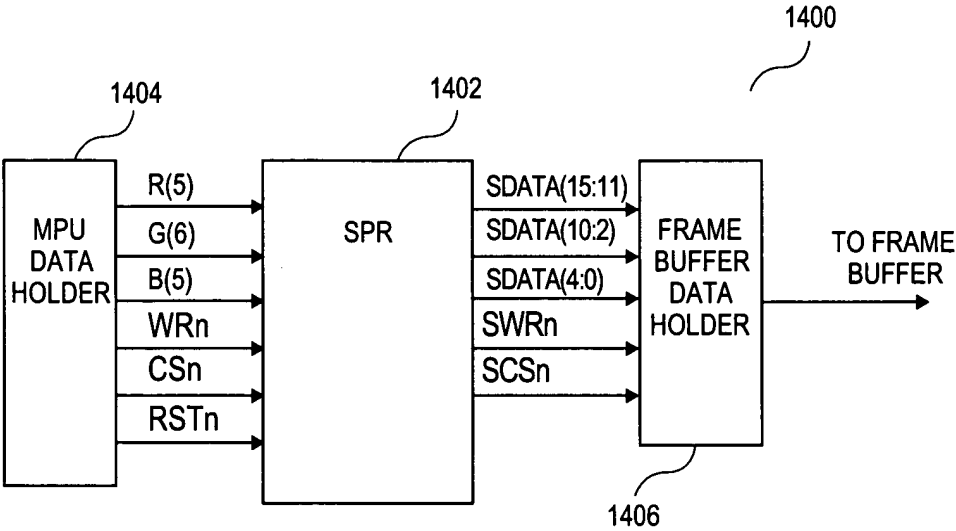


FIG. 14

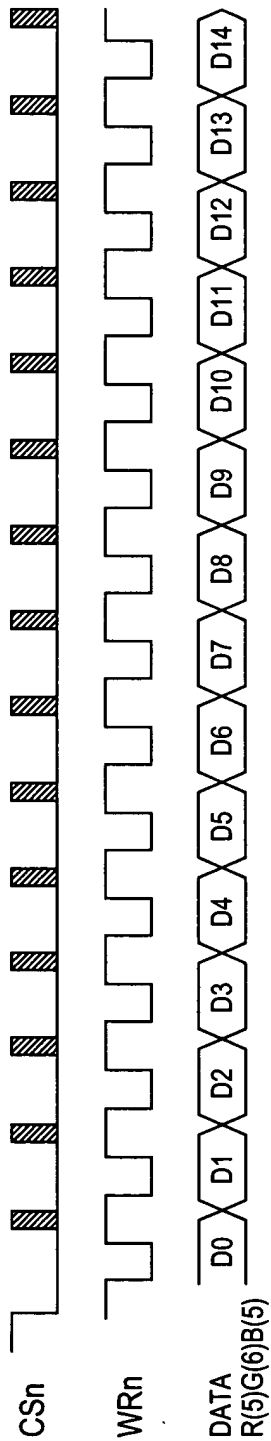


FIG. 15

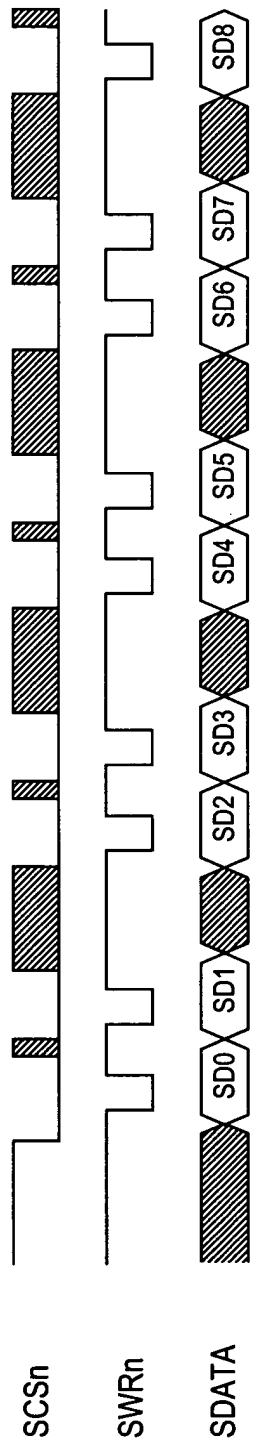


FIG. 16

[illegible]

FIG. 17

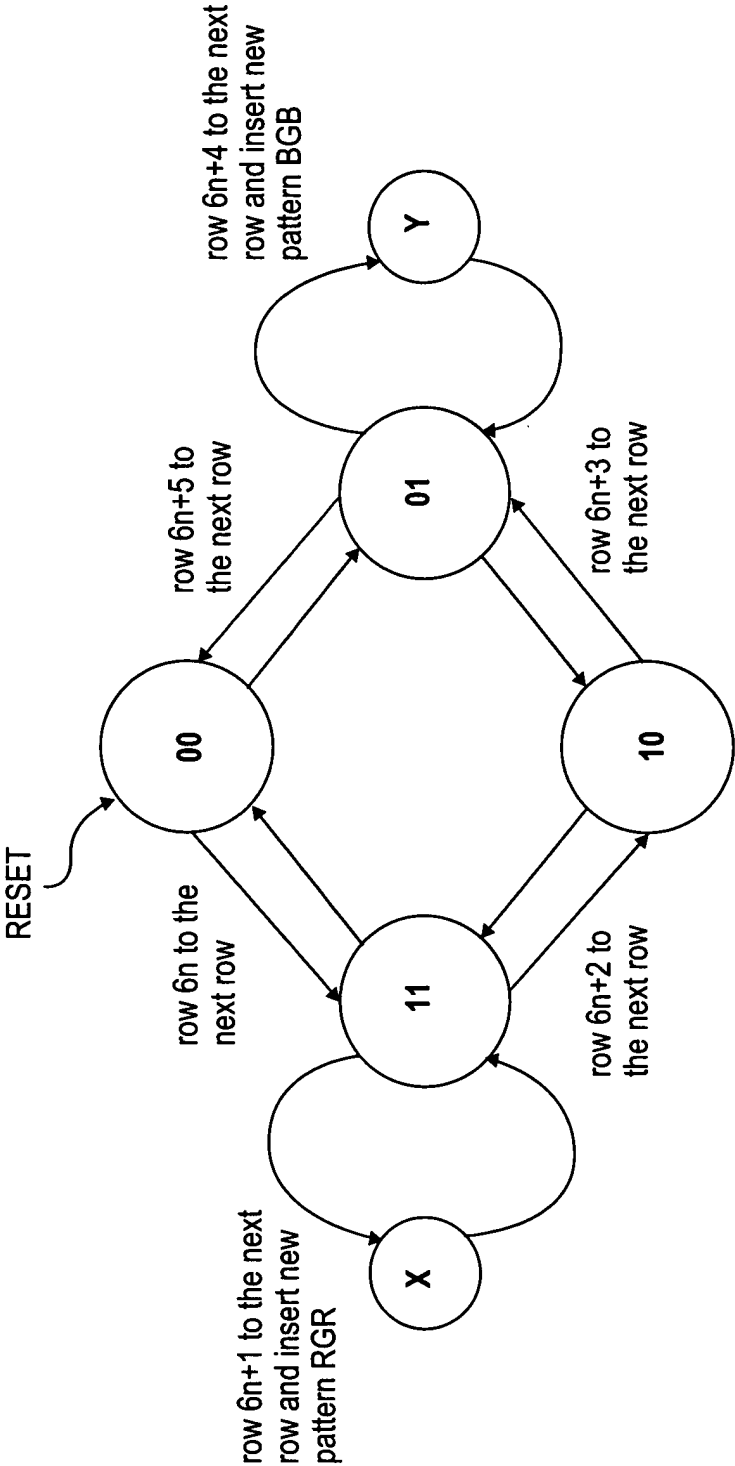


FIG. 18

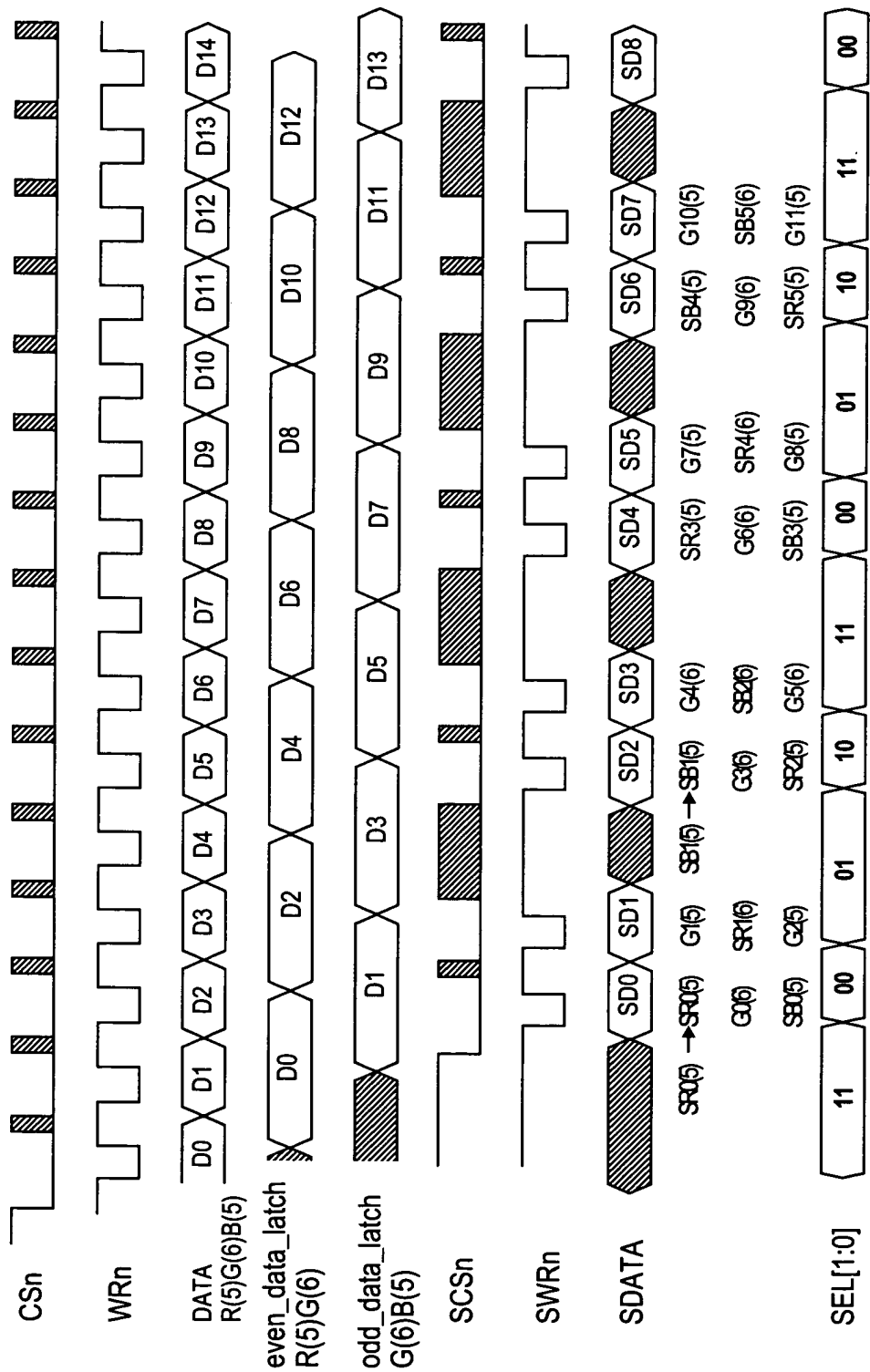


FIG. 19

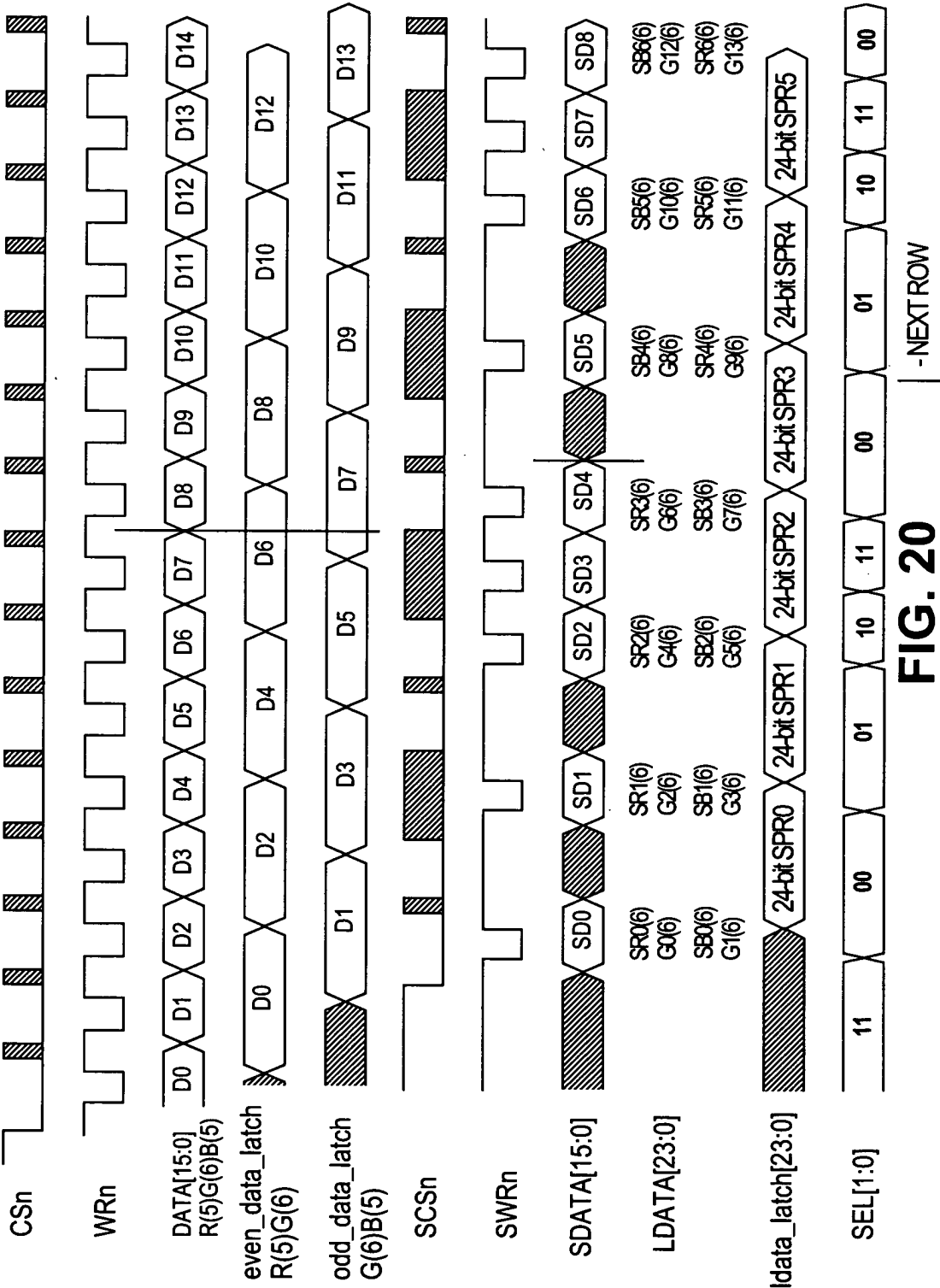


FIG. 20

-NEXT ROW

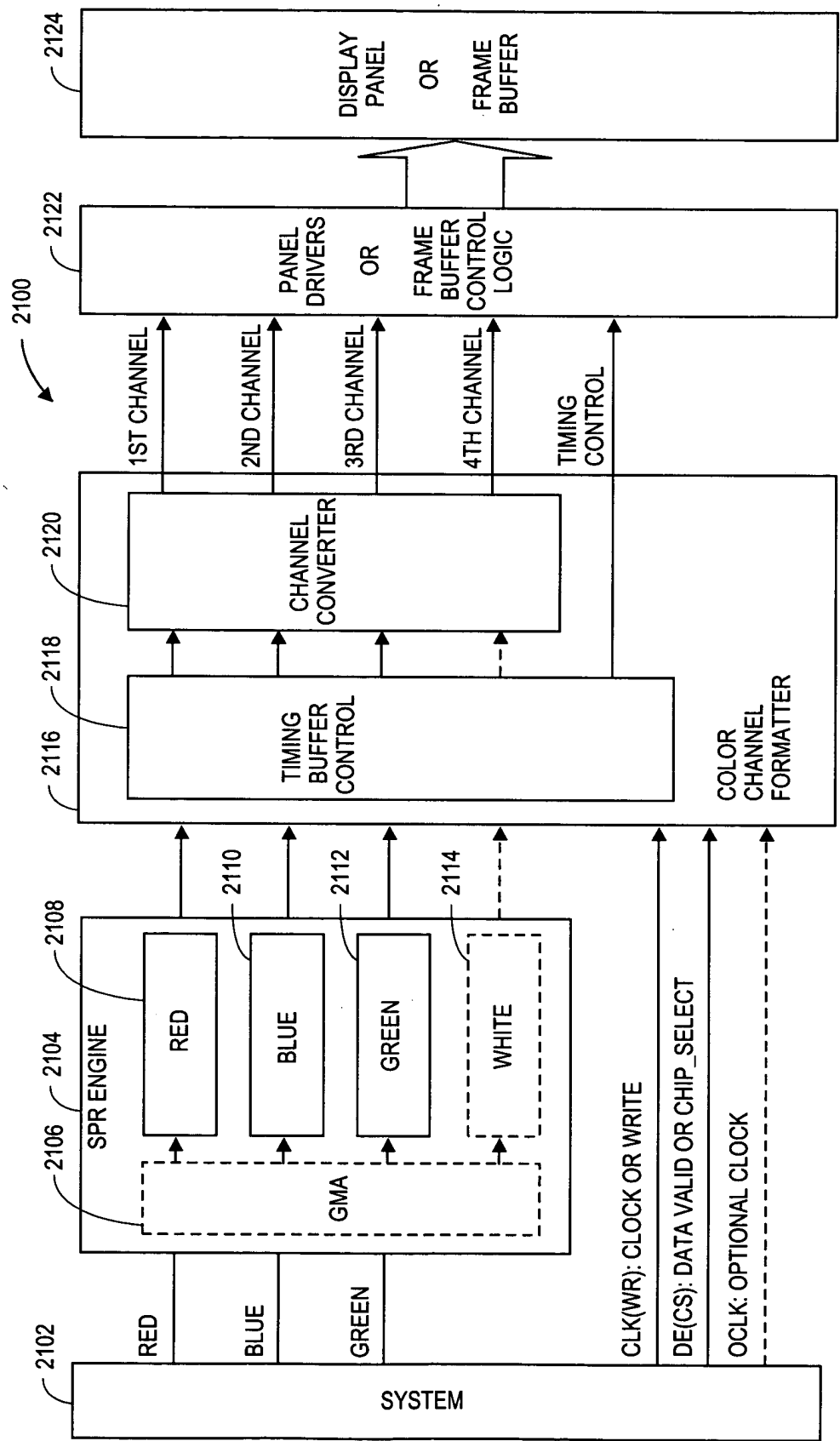


FIG. 21

Interface Formats		FIG. 22											
Frame-buffer interface format													
EXEMPLARY REPEATER GROUP		2-CHANNEL		3-CHANNEL		4-CHANNEL		4-CHANNEL		4-CHANNEL		4-CHANNEL	
		EVEN LINE	ODD LINE	EVEN LINE	ODD LINE	EVEN LINE	ODD LINE	EVEN LINE	ODD LINE	EVEN LINE	ODD LINE	EVEN LINE	ODD LINE
<div><div>R</div><div>B</div><div>G</div><div>G</div><div>B</div><div>R</div></div>	1ST CHANNEL			R	G								
	2ND CHANNEL			B	B								
	3RD CHANNEL			G	R								
	4TH CHANNEL												
<div><div>R</div><div>B</div><div>G</div><div>B</div><div>R</div><div>B</div></div>	1ST CHANNEL			R	G								
	2ND CHANNEL			B	B								
	3RD CHANNEL			G	R								
	4TH CHANNEL												
<div><div>R</div><div>G</div><div>B</div><div>G</div><div>B</div><div>R</div><div>G</div><div>G</div></div>	1ST CHANNEL	R, B	B, R	R, G, B, G	B, G, R, G	R, X, R, X	B, X, B, X	R	B				
	2ND CHANNEL	G, G	G, G	G, R, G, B	G, B, G, R	G, G, G, G	G, G, G, G						
	3RD CHANNEL			B, G, R, G	R, G, B, G	B, X, B, X	R, X, R, X	B	R				
	4TH CHANNEL							G	G				
<div><div>R</div><div>B</div><div>G</div><div>G</div><div>W</div><div>R</div></div>	1ST CHANNEL			R	G	R	G						
	2ND CHANNEL			B	W	W	B						
	3RD CHANNEL			G	R	G	R						
	4TH CHANNEL												
<div><div>R</div><div>B</div><div>G</div><div>W</div><div>R</div><div>B</div></div>	1ST CHANNEL			R, W	G, B			R	G				
	2ND CHANNEL			B, R	W, G			B	W				
	3RD CHANNEL			G, B	R, W			G	R				
	4TH CHANNEL							W	B				